## C. U. SHAH UNIVERSITY **Summer Examination-2022**

## **Subject Name : Integrated Circuits & Applications**

Subject Code : 4TE04ICA1			<b>Branch: B.Tech (Electrical)</b>		
Semest	er: 4	Date: 04/05/2022	Time: 11:00 To 02:00	Marks: 70	
Instruct (1) (2) (3) (4)	tions: Use Instr Drav Assu	of Programmable calculator & any uctions written on main answer bo v neat diagrams and figures (if neo me suitable data if needed.	y other electronic instrument is pr bok are strictly to be obeyed. cessary) at right places.	ohibited.	
Q-1	<ul> <li>a)</li> <li>b)</li> <li>c)</li> <li>d)</li> <li>e)</li> <li>f)</li> <li>g)</li> <li>h)</li> <li>i)</li> <li>j)</li> <li>k)</li> <li>l)</li> <li>m)</li> <li>n)</li> </ul>	Attempt the following question State the name of various buildin The 741 IC means that the opam applications. (True/False). For various temperature ranges t The bandwidth of 741 IC is 0.1 M The input impedance of an ideal (c) Few kilo ohm (d) Few hundred If slew rate of opamp is high it is The opamp 741 has biasing Eight. The integral of square wave is ra The differential amplifier conver (a) Square (b) pulse (c) Cosine (c) Decibel is the unit of gain ALL The 555 timer has only one oper State any two applications of 555 If 1 mv signal is given to open lo	hs: hg block of opamp 741C. p is designed for commercial he opamp has different IC. (True/ MHz (True/False) opamp is (a) Infinite (b) ed ohms. s much sensitive.(True/False) g pins. (a) One (b) Two (c) Three the sine wave into d) sine (a) Voltage (b) Current (c) Powe ating mode. (True/False) 5 timer circuit. bop opamp what will be the outpu guration using opamp.	(14) False) Zero (d) er (d) t	
Attemp	ot any	four questions from Q-2 to Q-8	;		
Q-2	(a) (b)	Attempt all questions Discuss the operation of 555 tim Draw the pin diagram of opamp	her in a stable mode. 741 C. Explain the function of ea	(14) (7) ch. (7)	

## Q-3 Attempt all questions

(a) State the name of any five parameters those are listed on opamp data (7)



(14)

sheet. Explain any two.

	(a)	State the characteristics of an ideal opamp	(7)
Q-4		Attempt all questions	(14)
	(a)	State and explain equivalent circuit of an opamp.	(7)
	(b)	Explain the inverting mode operation of opamp with negative feedback	(7)
Q-5		Attempt all questions	(14)
	(a)	Derive the equation of closed loop voltage gain for inverting configuration with feedback.	(7)
	(b)	State the name of any five parameters those are listed on opamp data sheet. Explain any two.	(7)
Q-6		Attempt all questions	(14)
-	(a)	Discuss the closed loop frequency response of an opamp.	(7)
	(b)	Discuss the low voltage a.c. voltmeter application using opamp.	(7)
0-7		Attempt all questions	$(\dot{1}\dot{4})$
× ·	(a)	Discuss the operation of summing amplifier using onamp	(7)
	(b)	Discuss the operation of integrator using opamp.	(7)
<b>O-8</b>		Attempt all questions	(14)
*	(a)	Briefly discuss about the following: (a) Gain bandwidth product (b)	(7)
	``	Slew rate (c) Common mode rejection ratio.	. /
	(b)	Discuss the operation of first order filter using an opamp.	(7)

